

In the Abstract:

Please amend the abstract of the disclosure as indicated below.

A dc-to-dc converter has two field effect transistors (~~35, 36~~) connected in series between an input terminal (~~37~~) and a ground terminal (~~38~~). Adjustment of the dead time when both transistors (~~35, 36~~) are off is carried out by providing Kelvin feedback connections (~~71, 72, 67, 68~~) directly across the drain (~~39, 44~~) and source (~~43, 40~~) of one or both of the transistors (~~35, 36~~), so bypassing signal line resistance and inductances.